Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
Li	381	703/22.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
1.2	335	703/13.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L3	541	703/14.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L4	66	703/20.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L5	171	703/21.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L6	5	(("6262711") or ("5768134") or ("5813984") or ("5953686") or ("3843132")). PN.	US-PGPUB; USPAT	OR	OFF	2006/01/06 10:27
L7	43	446/91.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L8	6	("6443796").URPN.	USPAT	OR	ON	2006/01/06 10:27
L9	18	("3484984" "3696548" "4348191" "4552541" "4556393" "4743202" "5013047" "5154615" "5411428" "5742486" "5779515" "5788553" "5853327" "5906369" "6190174" "6206745" "6227931").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/06 10:27
L10	1	("6043667").PN.	US-PGPUB; USPAT	OR	OFF	2006/01/06 10:27
LII	81	273/238.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L12	24	434/379.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L13	62	439/43.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L14	83	439/49.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L15	629	virtual adj model	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L16	667	(baseboard breadboard) and sensor and component	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L17	6	L15 and L16	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L18	310	L16 and model	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L19	213	L18 and structure	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L20	30	L18 and signature	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L21	14	L18 and voltmeter	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L22	3	L18 and ammeter	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L23	154	L19 and mount\$4	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L24	76	L23 and @ad<="20010205"	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L25	270	345/501.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
L26	426	703/1.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27

		Results
7.	((pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard))) and model) and block [All Sources(- All Sciences -)]	15
6.	((pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard))) and model) and component [All Sources(- All Sciences -)]	26
5.	(pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard))) and model [All Sources(- All Sciences -)]	44
4.	pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard)) [All Sources(- All Sciences -)]	67
3.	((pub-date > 1959 and pub-date < 2002 and FULL-TEXT(virtual model) and FULL-TEXT(component)) and display) and sensor [All Sources(- All Sciences -)]	20
2.	(pub-date > 1959 and pub-date < 2002 and FULL-TEXT(virtual model) and FULL-TEXT(component)) and display [All Sources(- All Sciences -)]	48
1.	pub-date > 1959 and pub-date < 2002 and FULL-TEXT(virtual model) and FULL-TEXT(component) [All Sources(- All Sciences -)]	99

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#3 ((virtual model <and>display)) <and> (pyr >= 1951 <and> pyr <= 2001)</and></and></and>	101
(((model <and>sensor)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)) <and> (((mount*<and>structure)<and> (baseboard<or>breadboard)) <and> (pyr >= 1951 <and> pyr <= 2001))</and></and></or></and></and></and></and></and></and></and>	52
((((model <and>sensor)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)) <and> (((mount*<and>structure)<and> (baseboard<or> = 2001)) <and> (((virtual model<and>display)) <and> (pyr >= 1951 <and> pyr <= 2001)) <and> pyr <= 2001))</and></and></and></and></and></or></and></and></and></and></and></and></and>	0
#6 ((virtual model <and>display)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)</and></and></and></and>	24
#7 ((virtual model <and>display)<and>magnet*<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)</and></and></and></and></and>	16
#8 ((virtual model <and>display)<and>conductor<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)</and></and></and></and></and>	2



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The USC Multiprocessor Testbed Project: Project Overview - Barroso, Iman, Jeong.. (1994) (Correct) (3 citations) a bus or a mesh (see Fig. 1)Whereas this physical **model** dominates, disagreement exists as to the approaches have been taken to verify a design: **breadboard** prototyping and software simulation. A them was valid only for the particular hardware. **Breadboard** prototypes are also extensively used in usc.edu/pub/CENG/1994/CENG-94-15.ps.Z

Yearly operation profile of the ATLAS SCT detectors - January Kondo (Correct) i T 2 E g 2k B T -exp 3 -1.5 Module **model** Two designs of the ATLAS barrel module are difference of the design is the shape of the TPG **baseboard** as shown in Figure 3 and Figure 4. 1.6 Thermal atlas.kek.jp/~kondo/sct/INDET-NO-203.ps

Modeling Analog Circuitry With VHDL - Mueller (1994) (Correct)

Modeling Analog Circuitry With Vhdl A Thesis Submitted

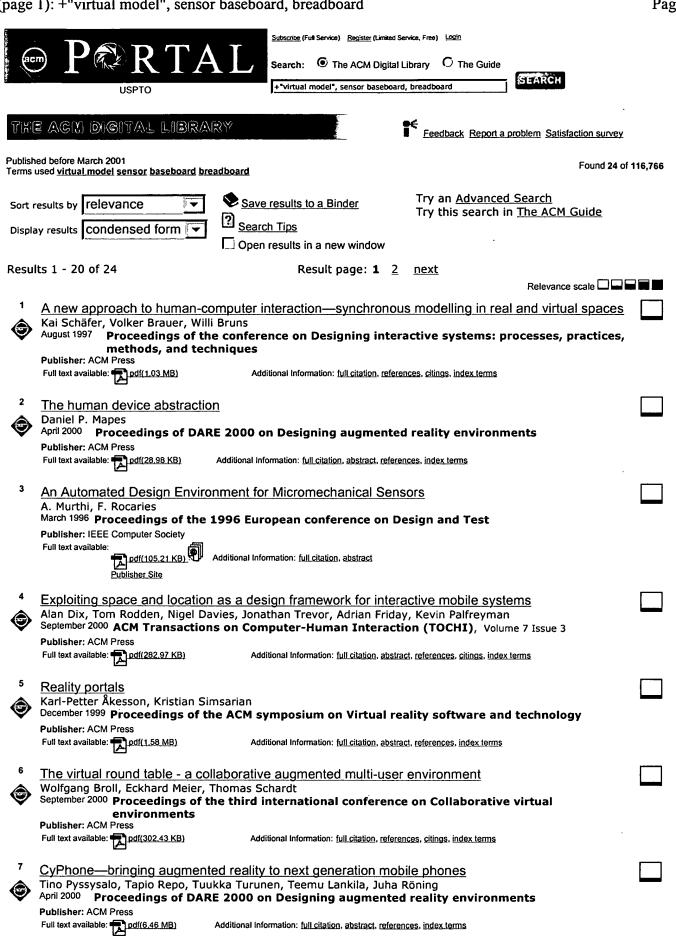
www.cse.nd.edu/pub/Reports/1994/tr-94-40.ps.gz

A Floor Boundary Sensor for Autonomous Robot Navigation - Bellutta And (Correct) vector ffl control and prediction module ffl system **model**. The image measurements are measurements, for the white of the floor and the black of the **baseboard**. In a different environment, a somewhat 4 alpha-bits.ai.mit.edu/people/bellutta/cv/fbs.e.ps.Z

Thermal simulation of the ATLAS SCT barrel module 27/5/97 - Thermal Simulation (Correct) safety margin for the thermal runaway. 1. Module **models** Eight different module geometries as listed in RAL in mid March, 1997 **model-1** BeO picture-frame **baseboard model-2 model-1** but with a longer PG basebaord atlas.kek.jp/~kondo/sct/INDET-NO-201.ps

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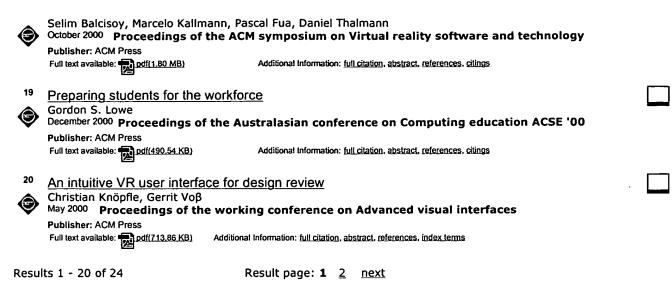
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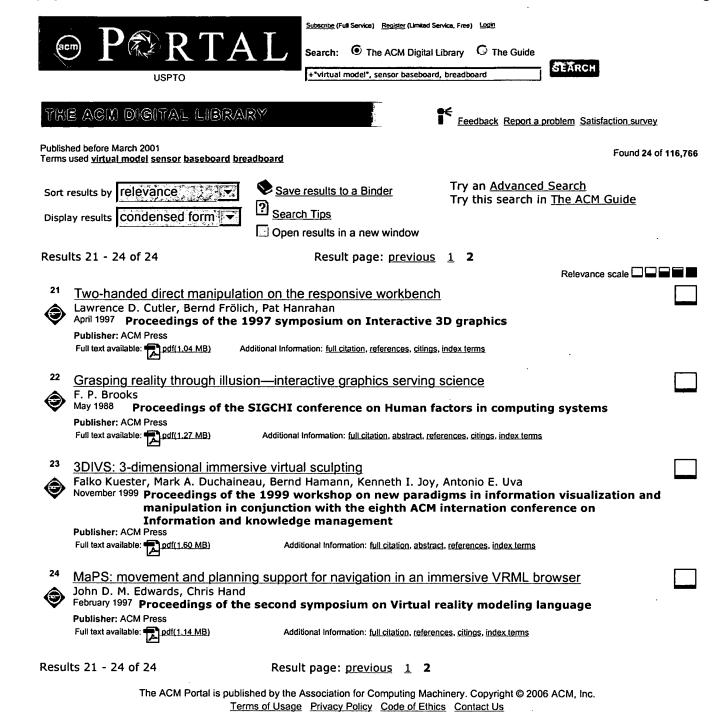
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